## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	244407	(critical fail\$3 violat \$3) with (path rout \$3 trac\$3 wir\$3) with (minimiz\$5 correct\$3 improv\$5 reduc\$4 fix\$3 compensat\$4 analysis analyz\$3 adjust\$4) OR (sta (static timing) near2 (closure analysis) static adj timing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:30
12	1182540	,	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:33
L3	163	(transistor adj fold \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:34
L4	2638	(transistor FET device MOSFET) with (width wide area dimension) with (redistribut\$4 distribut\$4 split\$4 divi\$4 arrang\$5 rearrang\$5) with (finger extension multi-finger \$3finger leg)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:34
L5	2109808	\$3) with (object transistor finger contact pad shap\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:34

L6	507697	(finger extension multi-finger \$3finger leg) with (add\$3 addition\$4 increas\$3 decreas\$3 subtract \$3 delet\$4 remov\$3 merg\$3 combin\$5 sum summing summation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:34
L7	10777	1 and (2 or 3 or 4) and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:37
L8	3380	1 and (2 or 3 or 4) and 5 and 6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 12:38
L9	45	8 and ("716"/\$.ccls. "324"/\$.ccls. "702"/\$. ccls. "326"/\$.ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/05 12:38
L10	30863	(transistor FET device MOSFET) with (finger multifinger \$3finger leg) with (add\$3 addition \$4 increas\$3 decreas \$3 subtract\$3 delet \$4 remov\$3 merg\$3 combin\$5)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 13:02
L11	704	1 and (2 or 3 or 4) and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/05 13:03
L12	9	11 and ("716"/\$. ccls. "324"/\$.ccls. "702"/\$.ccls. "326"/\$. ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/05 13:04
S1	14191	(critical fail\$3 violat \$3) with (path rout \$3 trac\$3 wir\$3) with (minimiz\$5 correct\$3 improv\$5 reduc\$4 fix\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:19

\$2	66110	finger with (reshap \$3 remov\$3 resiz\$3 ((redistribut\$4 modify\$3 modification minimiz \$5 decreas\$3 alter \$5 chang\$3) with (siz \$3 wide width area)))	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:19
S3	822337	rotat\$3 with (object transistor finger contact pad shap\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:20
S4	21	S1 and S2 and S3	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:20
S5	1	S4 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:21
<b>S</b> 6	26930	width with (transistor FET finger device) with (reshap\$3 remov\$3 resiz\$3 ((redistribut \$4 modify\$3 modification minimiz \$5 decreas\$3 alter \$5 chang\$3) with (siz \$3 wide width area)))	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:22
S7	192951	(finger extension) with (transistor FET device)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:23
S8	122	S1 and S6	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:23
S9	23	S8 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:23
S10	3	S9 and finger\$3	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:23
S11	5	("5416722"   "6253351"   "6434721"   "6446239"   "6528555").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/01 18:32
<b>S</b> 22	19067	(critical fail\$3 violat \$3) with (path rout \$3 trac\$3 wir\$3) with (minimiz\$5 correct\$3 improv\$5 reduc\$4 fix\$3 compensat\$4 analysis analyz\$3 adjust\$4) OR timing adj closure	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27

<b>S</b> 23	122485	(finger extension multi-finger \$3finger) with (reshap\$3 remov\$3 resiz\$3 ((redistribut \$4 modify\$3 modification minimiz \$5 decreas\$3 alter \$5 chang\$3 adjust \$4) with (siz\$3 wide width area)))	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
S24	319	S22 and S23	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
S25	20	S24 and "716"/\$. ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
S26	8	S24 and ("324"/\$. ccls. "702"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
S27	14	S24 and ("326"/\$. ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
S28	279	S24 not (S25 S26 S27)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 14:27
\$29	236899	(critical fail\$3 violat \$3) with (path rout \$3 trac\$3 wir\$3) with (minimiz\$5 correct\$3 improv\$5 reduc\$4 fix\$3 compensat\$4 analysis analyz\$3 adjust\$4) OR (sta (static timing) near2 (closure analysis))	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 15:11
S30	324	diffusion with (gap spac\$3 width) same (transistor FET device) with (finger extension multi- finger \$3finger)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 15:15
S31	16	S29 and S30	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 15:15
S32	8	(US-20030226122-\$ or US-20020188913-\$).did. or (US-5416722-\$ or US-7093208-\$ or US-6826733-\$ or US-6584599-\$ or US-6209122-\$ or US-6725438-\$).did.	US-PGPUB; USPAT	OR	OFF	2008/08/04 16:15

S33	5	S32 and (rotat\$4 pivot\$3 axis orthogonal\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:15
<b>S</b> 34	182259		US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:20
<b>S</b> 35	1656019	5.1	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:20
S36	205	S22 and S34 and S35	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:22
S37	44	S36 and ("716"/\$. ccls. "324"/\$.ccls. "702"/\$.ccls. "326"/\$. ccls. "438"/\$.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:23
S38	18	S37 and (finger extension multi- finger \$3finger)	US-PGPUB; USPAT; USOCR	OR	ON	2008/08/04 16:59

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